Memory Controllers for Mixed-Time-Criticality Systems: Architectures, Methodologies and Trade-offs (Embedded Systems)

Sven Goossens, Karthik Chandrasekar, Benny Akesson, Kees Goossens

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This book discusses the design and performance analysis of SDRAM controllers that cater to both real-time and best-effort applications, i.e. mixed-time-criticality memory controllers. The authors describe the state of the art, and then focus on an architecture template for reconfigurable memory controllers that addresses effectively the quickly evolving set of SDRAM standards, in terms of worst-case timing and power analysis, as well as implementation. A prototype implementation of the controller in SystemC and synthesizable VHDL for an FPGA development board are used as a proof of concept of the architecture template.

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